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Attorney Docket No. **10112971**

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	**Ching-Wei LIN**	Filed:	**10/3/2003**
Appl. No.:	**10/678,908**	Examiner:	**EVERHART, CARIDAD**
Conf. No.:	**2225**	Art Unit:	**2891**

Title: **PROCESS FOR PASSIVATING POLYSILICON AND PROCESS FOR FABRICATING POLYSILICON THIN FILM TRANSISTOR**

Date: **January 18, 2006**

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Commissioner for Patents
P.O. Box 1450
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RCE COVER SHEET

Attached hereto please find the following:

1. Request for Continued Examination Transmittal (1 page); and
2. Amendment (11 pages); and
3. Form PTO-2038 (1 page).

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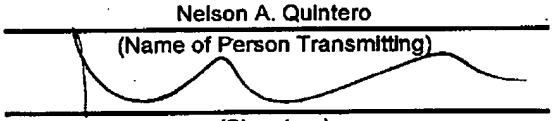
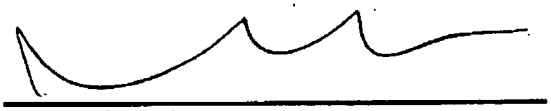


(Signature)

January 18, 2006
(Date)

Respectfully submitted,



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PAGE 1/15 * RCVD AT 1/18/2006 6:54:14 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-6/27 * DNIS:2738300 * CSID:310 401 6185 * DURATION (mm-ss):05-06